#### UTILITY Attorney Docket No. 206569US2 PATENT APPLICATION First Inventor or Application Identifier | Koichi MOTOIKE TRANSMITTAL SEMICONDUCTOR INTEGRATED CIRCUIT Assignee Name: KABUSHIKI KAISHA TOSHIBA jc860 U.S. PTO Assignee Address: 72, HORIKAWA-CHO, SAIWAI-KU, KAWASAKI-SHI, KANAGAWA-KEN, JAPAN APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent and Assistant Commissioner for Patents ADDRESS TO Box Patent Application Washington, DC 20231 polication contents Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing) ACCOMPANYING APPLICATION PARTS Assignment Papers (cover sheet & document(s)). 2. Specification Total Sheets 8. Application Data Sheet. See 37 CFR 1.76 9. 37 C.F.R. §3.73(b) Statement (when there is an assignee) □ Power of Attorney 3. Drawing(s) (35 U.S.C. 113) Total Sheets English Translation Document (if applicable) Information Disclosure Statement (IDS)/PTO-1449 □ Copies of IDS Total Pages 4. □ Oath or Declaration 12. Preliminary Amendment a. Newly executed (original or copy) White Advance Serial No. Postcard b. Copy from a prior application (37 C.F.R, §1.63(d)) (for continuation / divisional w/ box 17 completed) Certified Copy of Priority Document(s) (if foreign priority is claimed) DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). Applicant claims small entity status. See 37 CFR 1.27 CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix) 16. ■ Other: Notice of Priority, List of Inventor's Name and Address 6. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) a. Computer Readable Form (CRF) b. Specification or Sequence Listing on: i. CD-ROM or CD-R (2 copies); or ii. Paper c. Statements verifying identity of above copies 17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: □ Continuation Divisional □ Continuation-in-part (CIP) of prior application no .: Prior application information: Examiner: Group Art Unit: For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an eath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation of visuonal application and is hereby incorporated by reference. The incorporation <u>can only</u> be relied upon when a portion flats between indeventiny omitted from the submitted application parts. 18. Amend the specification by inserting before the first line the sentence: □ This application is a □ Continuation □ Division □ Continuation-in-part (CIP) of application Serial No. Filed on Which was published in English Which was not published in English □ This application claims priority of provisional application Serial No. Filed 19. CORRESPONDENCE ADDRESS

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206569US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

'INVENTOR(S) Koichi MOTOIKE

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT

# FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS

WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	20 - 20 =	0	× \$18 =	\$0.00
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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND. MAIER & NEUSTADT, P.C.

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DONAL C 171 Docket No. 206569US2

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

'IN RE APPLICATION OF: Koichi MOTOIKE

FILING DATE: Herewith

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT

## LIST OF INVENTOR'S NAME AND ADDRESS

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Listed below is the name and address of the inventor for the above-identified patent application.

Koichi MOTOIKE

Yokohama-Shi, JAPAN

A declaration containing all the necessary information will be submitted at a later date.

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